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(54) **METHOD AND PROCESSING APPARATUS FOR IMPLEMENTING IFFT BY USING FFT**

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(57) **ABSTRACT**

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A method for realizing IFFT by FFT is provided, which comprises: performing a left mirror permutation on an input data sequence, then performing FFT, and dividing the result with N, so as to obtain IFFT processing data; or performing FFT on an input data sequence, then performing a left mirror permutation on the result of FFT and being divided by N, so as to obtain IFFT processing data; the left mirror permutation is: inverting the sequence of the other data except the first data; the N is the length of the input data sequence. Also, an IFFT processing apparatus comprising an FFT computing unit and a left mirror permutation unit is provided.

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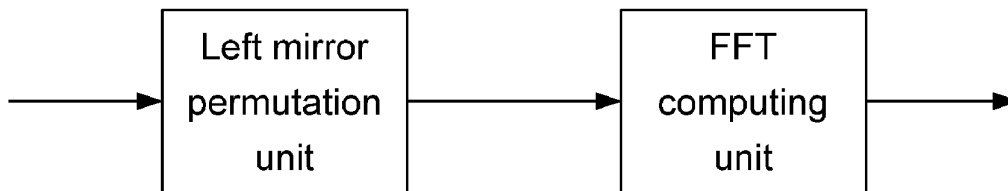


Fig. 1

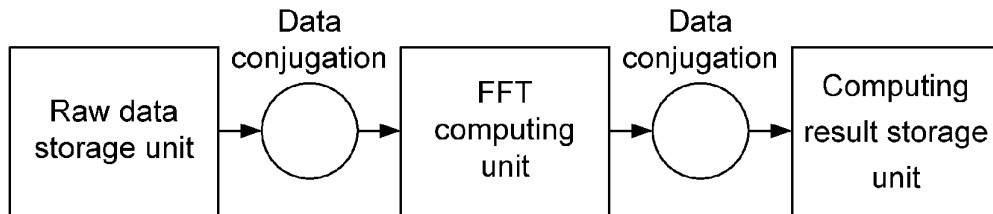


Fig. 2

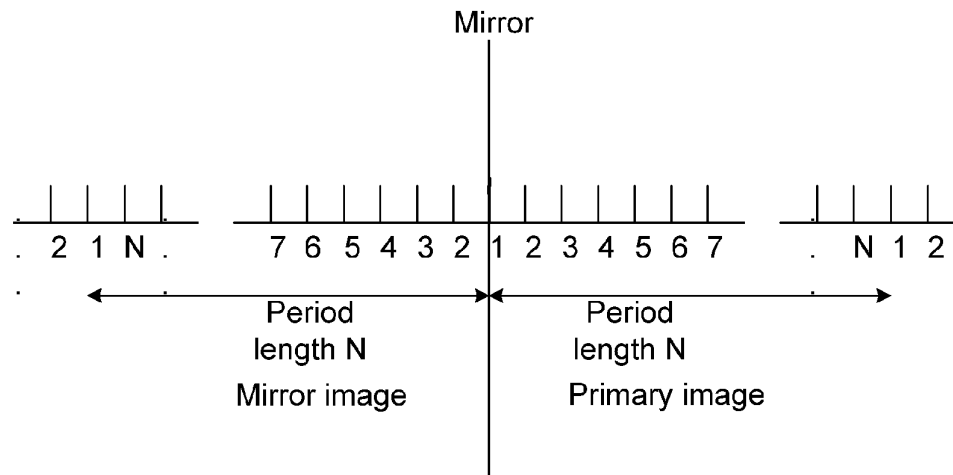


Fig. 3

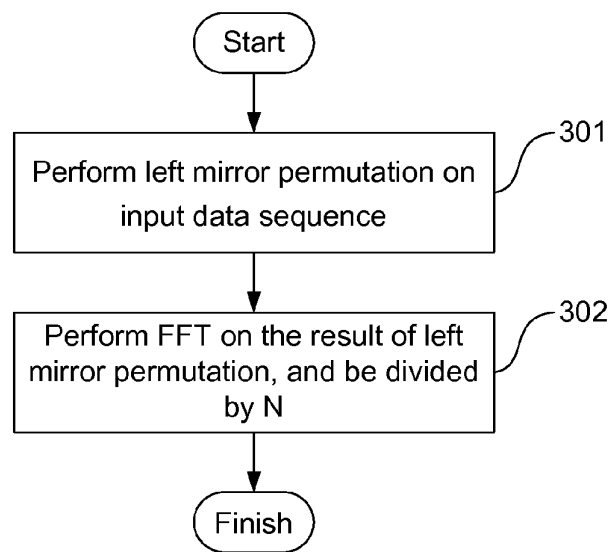


Fig. 4

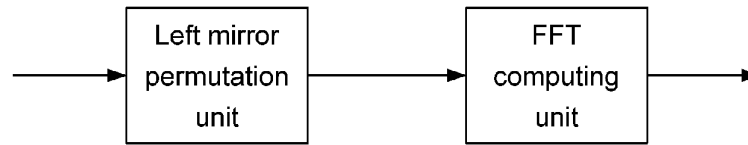


Fig. 5

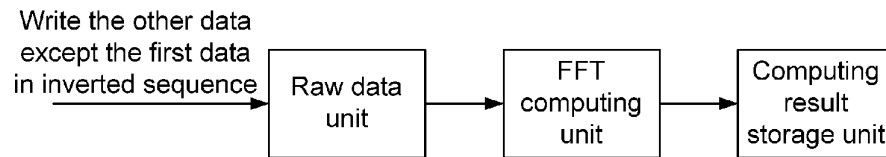


Fig. 6

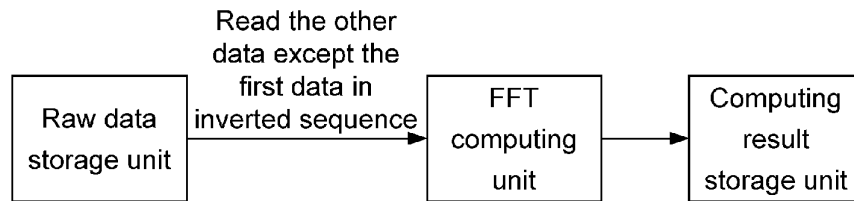


Fig. 7

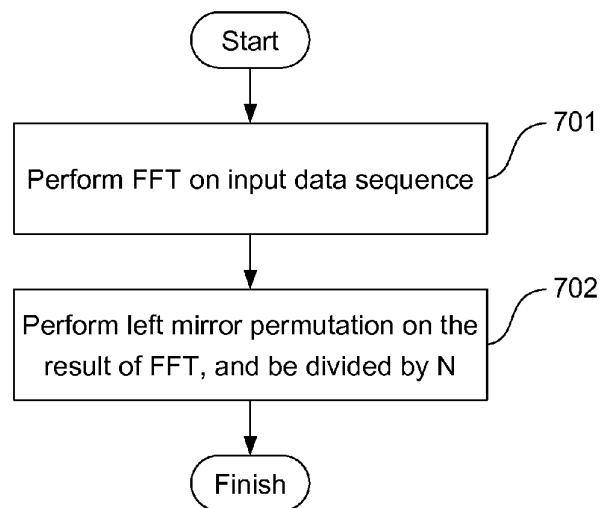


Fig. 8

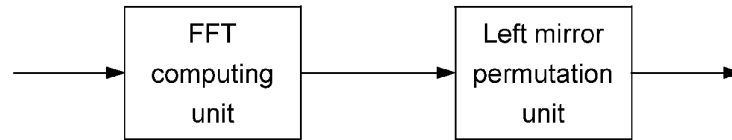


Fig. 9

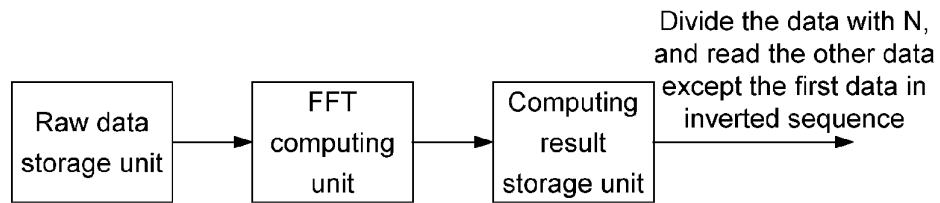


Fig. 10

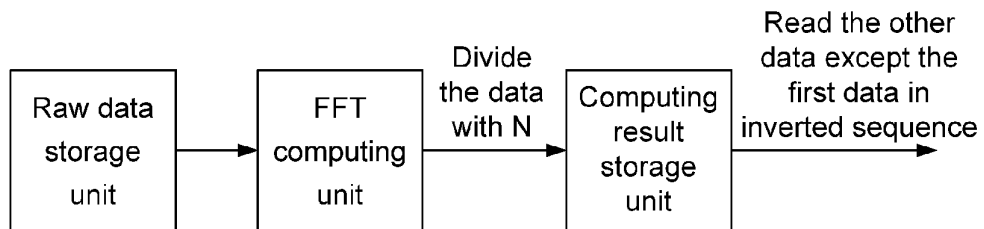


Fig. 11

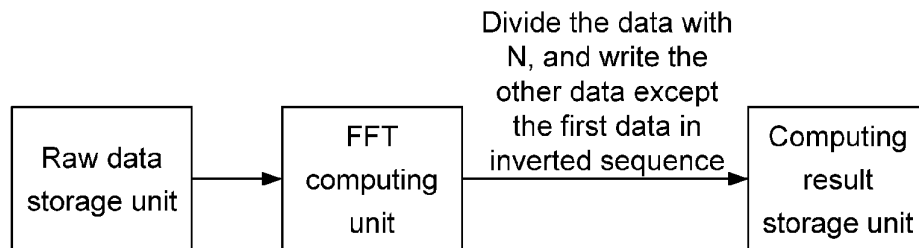
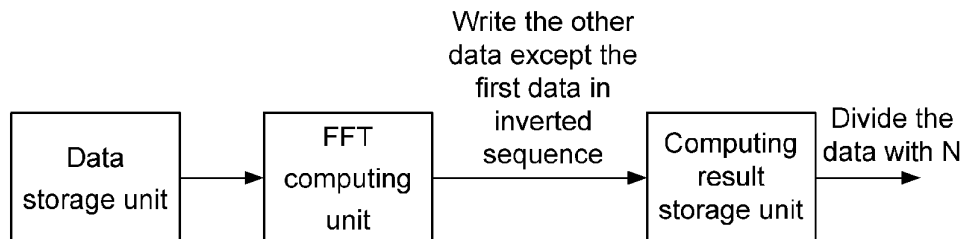


Fig. 12



METHOD AND PROCESSING APPARATUS FOR IMPLEMENTING IFFT BY USING FFT

TECHNICAL FIELD

[0001] The present invention relates to the field of digital signal processing, particularly to a method and processing apparatus for realizing Inverse Fast Fourier Transform (IFFT) by Fast Fourier Transform (FFT).

BACKGROUND

[0002] In digital signal processing, Discrete Fourier Transform (DFT) is an important transform in signal spectrum analysis and processing. Because the computing amount of directly computing DFT is direct proportional to the square of transform interval length N, and when N is relative large, the computing amount of DFT is too large. Before FFT is found, it is impractical to perform spectrum analysis and real time processing of signal directly by DFT. The situation has not changed fundamentally until a fast algorithm of DFT, namely FFT, was found in 1965.

[0003] FFT decomposes DFT with long sequences into DFT with short sequences, which reduces the computing amount greatly, and widely applies DFT to the fields of spectrum analysis, filter design and etc.

[0004] DFT of a finite long sequence x(n) signal with length N is shown as the following formula:

$$X(k) = DFT[x(n)] = \sum_{n=0}^{N-1} x(n)W_N^{nk} \quad (k = 0, 1, \dots, N-1)$$

[0005] Under the general condition that x(n) is a sequence of complex numbers, for a certain value k, computing value X(k) directly according to above formula needs N times of complex multiplication and (N-1) times of complex addition.

[0006] Because the coefficient

$$W_N^{nk} = e^{-j\frac{2\pi}{N}nk}$$

is a periodic function, the periodicity and symmetry thereof are as follows:

[0007] the symmetry of W_N^{nk} is $(W_N^{nk})^* = W_N^{-nk}$; and $W_N^{k+n/2} = -W_N^k$

[0008] the periodicity of W_N^{nk} is $W_N^{n(N+k)} = W_N^{k(N+n)} = W_N^{nk}$; and $W_N^{n(N-k)} = W_N^{k(N-n)} = W_N^{-nk}$

[0009] DFT of the x(n) signal can be decomposed into DFT with short sequences to the greatest extent by using the periodicity and symmetry of the W_N^{nk} .

[0010] FFT based on a butterfly unit is involved in common textbooks about digital signal processing, at the same time, the current textbooks also refer to how to fast compute Inverse Discrete Fourier Transform (IDFT) to digital signal by FFT, this computing method is called IFFT as follows:

[0011] setting DFT of the sequence x(n) signal to be X(k), then IDFT is:

$$x(n) = IDFT[X(k)] = \frac{1}{N} \sum_{k=0}^{N-1} X(k)W_N^{-nk} \quad (n = 0, 1, \dots, N-1)$$

[0012] conjugating IDFT is:

$$x^*(n) = \frac{1}{N} \sum_{k=0}^{N-1} X^*(k)W_N^{nk}$$

[0013] conjugating again is:

$$x(n) = \frac{1}{N} \left[\sum_{k=0}^{N-1} X^*(k)W_N^{nk} \right]^*$$

[0014] according to above description, the method for fast computing IDFT to a digital signal by FFT is as follows:

[0015] step 1: conjugating X(k) to obtain X*(k);

[0016] step 2: performing FFT operation on X*(k);

[0017] step 3: conjugating the operation result and being divided by N to obtain x(n).

[0018] As shown in FIG. 1, an IFFT processor of the prior art computes IDFT to a digital signal by an FFT computing unit; as can be seen from the drawing, the IFFT processing procedure of the prior art is: firstly performing a conjugating operation on a raw data, then performing FFT to the data by using the FFT computing unit, and performing a conjugating operation on the result data, so as to obtain the computing result of IDFT and store it.

[0019] This method cleverly makes use of the property of conjugate symmetry to compute IDFT by FFT, and is featured by using a known rotation factor W_N^{nk} of FFT, there is no need to compute a new rotation factor W_N^{-nk} , but still need to conjugate the input and output data, which causes the complication of processing work and reduces the processing efficiency.

SUMMARY

[0020] The present invention provides a method and processing apparatus for realizing IFFT by FFT, aiming at solving the technical problem, which does not need the conjugating operation before and after FFT, improving the utilization ratio of resources and the processing efficiency.

[0021] In order to achieve the purpose above, the technical solution of the present invention is realized as follows.

[0022] The present invention provides a method for realizing IFFT by FFT, which comprises:

[0023] performing a left mirror permutation on an input data sequence, then performing FFT, and dividing the result with N, so as to obtain IFFT processing data;

[0024] the left mirror permutation is: inverting the sequence of the other data except the first data; the N is the length of the input data sequence.

[0025] Further, the method specifically may comprise:

[0026] in a storage process of the input data sequence, storing the other data except the first data in inverted sequence when writing the input data;

[0027] orderly reading the input data according to the storage sequence, performing FFT, and dividing the result with N, so as to obtain the IFFT processing data.

[0028] Further, the method specifically may comprise:

[0029] in the storage process of input data sequence, storing according to the input data sequence;

[0030] when reading the input data, firstly reading the first input data, then reading the other input data from back to front, namely, in inverted sequence, then performing FFT, and dividing the result with N, so as to obtain IFFT processing data.

[0031] The present invention further provides a method for realizing IFFT by FFT, which comprises:

[0032] performing FFT on an input data sequence, then performing a left mirror permutation on the result of FFT and dividing with N, so as to obtain IFFT processing data;

[0033] the left mirror permutation is: inverting the sequence of the other data except the first data; the N is the length of the input data sequence.

[0034] Further, the method specifically may comprise:

[0035] performing FFT on the input data sequence, after storing the result data sequence of FFT, when reading the result data sequence of FFT, firstly reading the first result data, then reading the other result data from back to front, namely, in inverted sequence, and dividing all the result data with N, so as to obtain the IFFT processing data.

[0036] Further, the method specifically may comprise:

[0037] performing FFT on the input data sequence, after dividing the result data sequence of FFT with N and storing, when reading the result data sequence of FFT, firstly reading the first result data, then reading the other result data from back to front, namely, in inverted sequence, so as to obtain the IFFT processing data.

[0038] Further, the method specifically may comprise:

[0039] performing FFT on the input data sequence, dividing the obtained result data sequence of FFT with N, and storing the other data except the first data in inverted sequence; and reading according to the storage sequence when reading the result data sequence of FFT, so as to obtain IFFT processing data.

[0040] Further, the method specifically may comprise:

[0041] performing FFT on the input data sequence, to the obtained result data sequence of FFT, storing the other data except the first data in inverted sequence; reading according to the storage sequence when reading the result data sequence of FFT, and dividing with N, so as to obtain IFFT processing data.

[0042] The present invention further provides an IFFT processing apparatus, which comprises an FFT computing unit and a left mirror permutation unit;

[0043] the left mirror permutation unit is used for receiving an input data sequence, inverting the sequence of the other data except the first data, and outputting the data to the FFT computing unit;

[0044] the FFT computing unit is used for receiving the data output by the left mirror permutation unit, performing FFT, and dividing the result with N, so as to obtain IFFT processing data;

[0045] the N is the length of the input data sequence.

[0046] The left mirror permutation unit may be realized by a raw data storage unit.

[0047] The present invention further provides an IFFT processing apparatus, which comprises an FFT computing unit and a left mirror permutation unit;

[0048] the FFT computing unit is used for performing FFT on an input data sequence, and outputting a result data sequence of FFT to the left mirror permutation unit;

[0049] the left mirror permutation unit is used for receiving the result data sequence of FFT output by the FFT computing unit, inverting the sequence of the other data except the first data and dividing with N, so as to obtain IFFT processing data;

[0050] the N is the length of the input data sequence.

[0051] The left mirror permutation unit may comprise a computing result storage unit and a divider which are interconnected;

[0052] Wherein, the computing result storage unit is used for performing the operation of inverting the data sequence;

[0053] the divider is used for performing the operation of dividing the data related to inverting sequence with N.

[0054] The connection relationship between the computing result storage unit and the divider may be: an output side of the computing result storage unit is connected with an input side of the divider; or, an input side of the computing result storage unit is connected with an output side of the divider.

[0055] Compared with the prior art, the present invention does not need to conjugate the input and output data any more, IFFT can be realized just by rearranging input or output data in relatively simple processing way, the utilization ratio of resources and the processing efficiency is improved.

BRIEF DESCRIPTION OF THE DRAWING

[0056] FIG. 1 shows a schematic diagram of realizing IFFT in the prior art;

[0057] FIG. 2 shows a mirror schematic diagram of a discrete orderly sequence;

[0058] FIG. 3 shows a flow chart of a method in embodiment 1 of the present invention;

[0059] FIG. 4 shows a schematic diagram of an apparatus in embodiment 1 of the present invention;

[0060] FIG. 5 shows a schematic diagram of the first way in embodiment 1 of the present invention;

[0061] FIG. 6 shows a schematic diagram of the second way in embodiment 1 of the present invention;

[0062] FIG. 7 shows a flow chart of a method in embodiment 2 of the present invention;

[0063] FIG. 8 shows a schematic diagram of an apparatus in embodiment 2 of the present invention;

[0064] FIG. 9 shows a schematic diagram of the first way in embodiment 2 of the present invention;

[0065] FIG. 10 shows a schematic diagram of the second way in embodiment 2 of the present invention;

[0066] FIG. 11 shows a schematic diagram of the third way in embodiment 2 of the present invention;

[0067] FIG. 12 shows a schematic diagram of the fourth way in embodiment 2 of the present invention.

DETAILED DESCRIPTION

[0068] In the present invention, there is no need to conjugate input and output data, but just to perform left mirror permutation on the input data or output data. The left mirror permutation is: inverting the sequence of other data except the first data, namely, IFFT can be realized by rearranging the input or output data in a relatively simple processing way.

[0069] In the following, the present invention is described in detail with reference to the drawings and embodiments.

[0079] because $W_N^{mN}=1$, then the result of multiplying each element in the right matrix of the formula (1) by W_N^{mN} is not changed, this is the periodicity of W_N^l , namely, $W_N^l=W_N^{l+mN}$; in this way, multiplying all the elements of the second column by W_N^{2N} , multiplying all the elements of the third column by W_N^{4N} , ... and multiplying all the elements of the Nth column by $W_N^{(N-1)N}$, then right part of the formula (1) can be simplified into:

$$\begin{pmatrix} W_N^0 & W_N^0 & W_N^0 & \dots & W_N^0 & W_N^0 \\ W_N^0 & W_N^1 & W_N^2 & \dots & W_N^{(N-2)} & W_N^{(N-1)} \\ W_N^0 & W_N^2 & W_N^4 & \dots & W_N^{2(N-2)} & W_N^{2(N-1)} \\ \dots & \dots & \dots & \dots & \dots & \dots \\ W_N^0 & W_N^{N-2} & W_N^{2(N-2)} & \dots & W_N^{(N-2)(N-2)} & W_N^{(N-2)(N-1)} \\ W_N^0 & W_N^{N-1} & W_N^{2(N-1)} & \dots & W_N^{(N-1)(N-2)} & W_N^{(N-1)(N-1)} \end{pmatrix}$$

[0080] obviously, this matrix is a DFT matrix, which proves that IDFT and DFT are left mirror images of each other.

[0081] As the above conclusion is drawn, fast computing IDFT by FFT can be performed according to the character, thereby realizing IFFT.

[0082] Setting DFT of the sequence $x(n)$ to be $X(k)$, then IDFT is:

$$x(n) = IDFT[X(k)] = \frac{1}{N} \sum_{k=0}^{N-1} X(k)W_N^{-nk} \quad (n = 0, 1, \dots, N-1)$$

[0083] namely:

$$x(n) = \frac{1}{N} (X(0), x(1) \dots x(N-1)) \quad (2)$$

$$\begin{pmatrix} W_N^0 & W_N^0 & W_N^0 & \dots & W_N^0 & W_N^0 \\ W_N^0 & W_N^{-1} & W_N^{-2} & \dots & W_N^{-(N-2)} & W_N^{-(N-1)} \\ W_N^0 & W_N^{-2} & W_N^{-4} & \dots & W_N^{-2(N-2)} & W_N^{-2(N-1)} \\ \dots & \dots & \dots & \dots & \dots & \dots \\ W_N^0 & W_N^{-(N-2)} & W_N^{-2(N-2)} & \dots & W_N^{-(N-2)(N-2)} & W_N^{-(N-2)(N-1)} \\ W_N^0 & W_N^{-(N-1)} & W_N^{-2(N-1)} & \dots & W_N^{-(N-1)(N-2)} & W_N^{-(N-1)(N-1)} \end{pmatrix}$$

[0084] obtaining from the character 3 of the permutation matrix:

$$(X(0), x(1) \dots x(N-1)) = X(0), x(1) \dots x(N-1))$$

$$\begin{pmatrix} 1 & 0 & 0 & \dots & 0 & 0 \\ 0 & 0 & 0 & \dots & 0 & 1 \\ 0 & 0 & 0 & \dots & 1 & 0 \\ \dots & \dots & \dots & \dots & \dots & \dots \\ 0 & 0 & 1 & \dots & 0 & 0 \\ 0 & 1 & 0 & \dots & 0 & 0 \end{pmatrix} \begin{pmatrix} 1 & 0 & 0 & \dots & 0 & 0 \\ 0 & 0 & 0 & \dots & 0 & 1 \\ 0 & 0 & 0 & \dots & 1 & 0 \\ \dots & \dots & \dots & \dots & \dots & \dots \\ 0 & 0 & 1 & \dots & 0 & 0 \\ 0 & 1 & 0 & \dots & 0 & 0 \end{pmatrix}$$

[0085] namely:

$$(X(0), x(1) \dots x(N-1)) = \quad (3)$$

$$\begin{pmatrix} 1 & 0 & 0 & \dots & 0 & 0 \\ 0 & 0 & 0 & \dots & 0 & 1 \\ 0 & 0 & 0 & \dots & 1 & 0 \\ \dots & \dots & \dots & \dots & \dots & \dots \\ 0 & 0 & 1 & \dots & 0 & 0 \\ 0 & 1 & 0 & \dots & 0 & 0 \end{pmatrix}$$

[0086] then obtaining by substituting the formula (3) into the formula (2):

$$x(n) = \frac{1}{N} (X(0), x(N-1) \dots x(1)) \begin{pmatrix} 1 & 0 & 0 & \dots & 0 & 0 \\ 0 & 0 & 0 & \dots & 0 & 1 \\ 0 & 0 & 0 & \dots & 1 & 0 \\ \dots & \dots & \dots & \dots & \dots & \dots \\ 0 & 0 & 1 & \dots & 0 & 0 \\ 0 & 1 & 0 & \dots & 0 & 0 \end{pmatrix}$$

$$\begin{pmatrix} W_N^0 & W_N^0 & W_N^0 & \dots & W_N^0 & W_N^0 \\ W_N^0 & W_N^{-1} & W_N^{-2} & \dots & W_N^{-(N-2)} & W_N^{-(N-1)} \\ W_N^0 & W_N^{-2} & W_N^{-4} & \dots & W_N^{-2(N-2)} & W_N^{-2(N-1)} \\ \dots & \dots & \dots & \dots & \dots & \dots \\ W_N^0 & W_N^{-(N-2)} & W_N^{-2(N-2)} & \dots & W_N^{-(N-2)(N-2)} & W_N^{-(N-2)(N-1)} \\ W_N^0 & W_N^{-(N-1)} & W_N^{-2(N-1)} & \dots & W_N^{-(N-1)(N-2)} & W_N^{-(N-1)(N-1)} \end{pmatrix}$$

[0087] because the DFT matrix can be obtained by left multiplying the left mirror image permutation matrix by the IDFT matrix, so:

$$x(n) = \frac{1}{N} X(0), x(N-1) \dots x(1)) \quad (4)$$

$$\begin{pmatrix} W_N^0 & W_N^0 & W_N^0 & \dots & W_N^0 & W_N^0 \\ W_N^0 & W_N^1 & W_N^2 & \dots & W_N^{(N-2)} & W_N^{(N-1)} \\ W_N^0 & W_N^2 & W_N^4 & \dots & W_N^{2(N-2)} & W_N^{2(N-1)} \\ \dots & \dots & \dots & \dots & \dots & \dots \\ W_N^0 & W_N^{N-2} & W_N^{2(N-2)} & \dots & W_N^{(N-2)(N-2)} & W_N^{(N-2)(N-1)} \\ W_N^0 & W_N^{N-1} & W_N^{2(N-1)} & \dots & W_N^{(N-1)(N-2)} & W_N^{(N-1)(N-1)} \end{pmatrix}$$

[0088] so according to the formula (4), it is concluded that IDFT can be obtained by performing DFT after performing the left mirror permutation on the input data.

[0089] Likewise, because the IDFT matrix is a symmetric matrix, so the results of left or right multiplying the left mirror image permutation matrix by the IDFT matrix are the same; and it is easy to prove that IDFT can be obtained by performing the left mirror permutation after performing DFT on the input data.

Embodiment 1

[0090] As shown in FIG. 3, the method in the embodiment of the present invention comprises the following steps:

[0091] step 301: performing a left mirror permutation on an input data sequence;

[0092] step 302: performing FFT on the result of the left mirror permutation, and dividing with N, so as to obtain IFFT processing data.

[0093] Wherein, the left mirror permutation is: inverting the sequence of other data except the first data, i.e., setting the other data in inverted sequence except the first data;

[0094] The N is the length of the input data sequence (namely, the number of the input data).

[0095] As shown in FIG. 4, an IFFT computing unit comprises a left mirror permutation unit and an FFT computing unit;

[0096] wherein, the left mirror permutation unit is used for receiving the input data sequence, inverting the sequence of the other data except the first data, and outputting the data to the FFT computing unit;

[0097] the FFT computing unit is used for receiving the data output by the left mirror permutation unit, performing FFT, and dividing the result with N, so as to obtain the IFFT processing data.

[0098] In practical application, the function of the left mirror permutation unit entity can be realized by a storage unit with the function of data storing and providing, for example, a raw data storage unit. When performing operations related to the left mirror permutation, aiming at the raw data storage unit, the left mirror permutation can be realized by directly adjusting the sequence of reading/writing data in the process of inputting and outputting the data, specifically, multiple ways can be adopted to realize aiming at the embodiment, for example:

[0099] the first way: with reference to FIG. 5, in the storage process of an input data sequence, when writing the input data into the specified address in the raw data storage unit, storing the other data except the first data in inverted sequence; when using the FFT computing unit to compute, orderly reading the input data from the raw data storage unit according to the storage sequence, performing FFT in the FFT computing unit, and dividing the result with N, so as to obtain the IFFT processing data and storing in the computing result storage unit;

[0100] the second way: with reference to FIG. 6, in the storage process of an input data sequence, storing according to the input data sequence in the raw data storage unit; when reading the input data from the raw data storage unit, firstly reading the first input data, then reading the other input data from back to front, namely, in inverted sequence, performing FFT in the FFT computing unit, and dividing the result with N, so as to obtain the IFFT processing data and storing in the computing result storage unit.

Embodiment 2

[0101] As shown in FIG. 7, the method in the embodiment of the present invention comprises the following steps:

[0102] step 701: performing FFT on an input data sequence;

[0103] step 702: performing the left mirror permutation on the result of FFT, and dividing with N, so as to obtain the IFFT processing data.

[0104] Wherein, the left mirror permutation is: inverting the sequence of the other data except the first data, i.e., setting the other data in inverted sequence except the first data;

[0105] the N is the length of the input data sequence (namely, the number of the input data).

[0106] As shown in FIG. 8, the IFFT computing unit comprises a left mirror permutation unit and an FFT computing unit;

[0107] wherein, the FFT computing unit is used for performing FFT on the input data sequence, and outputting the result data sequence of FFT to the left mirror permutation unit;

[0108] the left mirror permutation unit is used for receiving the result data sequence of FFT output by the FFT computing unit, inverting the sequence of the other data except the first data, and dividing with N, so as to obtain the IFFT processing data.

[0109] Being similar to the principle in the embodiment 1, in practical application, the function of the left mirror permutation unit entity can be realized by a storage unit (for example, a computing result storage unit) with the function of data storing and providing and a divider connected therewith. When performing operations related to the left mirror permutation, aiming at the raw data storage unit, the left mirror permutation can be realized by directly adjusting the sequence of reading/writing data in the process of inputting and outputting the data, specifically, multiple ways can be adopted to realize aiming at the embodiment, for example:

[0110] the first way: with reference to FIG. 9, taking out the input data sequence from the raw data storage unit, performing FFT in the FFT computing unit, and storing the result data sequence of FFT in the computing result storage unit; when reading the result data sequence of FFT from the computing result storage unit, firstly reading the first result data, then reading other result data from back to front, namely, in inverted sequence, and dividing all the result data with N by the divider, so as to obtain the IFFT processing data;

[0111] the second way: with reference to FIG. 10, taking out the input data sequence from the raw data storage unit, performing FFT in the FFT computing unit, dividing the result data sequence of FFT with N by the divider and then storing in the computing result storage unit; when reading the result data sequence of FFT from the computing result storage unit, firstly reading the first result data, then reading other result data from back to front, namely, in inverted sequence, so as to obtain the IFFT processing data;

[0112] the third way: with reference to FIG. 11, taking out the input data sequence from the raw data storage unit, performing FFT in the FFT computing unit, dividing the obtained result data sequence of FFT with N by the divider, and storing the other data except the first data in the computing result storage unit in inverted sequence; reading according to the storage sequence when reading the result data sequence of FFT, so as to obtain the IFFT processing data;

[0113] the fourth way: with reference to FIG. 12, taking out the input data sequence from the raw data storage unit, performing FFT in the FFT computing unit, for the obtained result data sequence of FFT, storing the other data except the first data in the computing result storage unit in inverted sequence; reading according to the storage sequence when reading the result data sequence of FFT, and sending to the divider to divide with N, so as to obtain the IFFT processing data.

[0114] It can be seen from the ways above that, whatever the connection relationship between the computing result storage unit and the divider is, for example: the output side of the computing result storage unit is connected with the input side of the divider, or the input side of the computing result storage unit is connected with the output side of the divider, the functions realized by the left mirror permutation unit entity can be realized by combining the computing result storage unit with the divider: aiming at the result data sequence of FFT, inverting the sequence of the other data except the first data, and dividing with N, so as to obtain the IFFT processing data.

[0115] The method and apparatus of the present invention can be widely applied to a Digital Signal Processor (DSP), and Field Programmable Gate Array (FPGA) and the like that using FFT/IFFT to process. Though the present invention is not complex in theory and demonstration process, the conclusion is very novel and simple; the relation of left mirror image between DFT and IDFT, and the method by using the same method to compute IDFT so as to realize IFFT cannot be retrieved in all the current textbooks, resources and Internet. Compared with the traditional method, there is no need to conjugate input and output data, but just rearrange the order of the input data or output data, thereby reducing the steps of operation and improving the utilization ratio of resources and the processing efficiency.

[0116] Certainly, the present invention may have many other embodiments. For those skilled in the art, various corresponding modifications and equivalent deformations may be made according to the present invention without departing from the spirit and the substance of the present invention, but these corresponding modifications and equivalent deformations are within the protection scope of the claims of the present invention.

What is claimed is:

1. A method for realizing Inverse Fast Fourier Transform (IFFT) by Fast Fourier Transform (FFT), comprising:

performing a left mirror permutation on an input data sequence, then performing FFT, and dividing a result with N, so as to obtain IFFT processing data;

the left mirror permutation is: inverting the sequence of the other data except the first data; the N is the length of the input data sequence.

2. The method according to claim 1, wherein performing the left mirror permutation on the input data sequence is: in a storage process of the input data sequence, storing the other data except the first data in inverted sequence when writing the input data; and orderly reading the input data according to the storage sequence.

3. The method according to claim 1, wherein performing the left mirror permutation on the input data sequence is: in a storage process of the input data sequence, storing according to the input data sequence; when reading the input data, firstly reading the first input data, then reading the other input data from back to front, namely, in inverted sequence.

4. A method for realizing IFFT by FFT, comprising: performing FFT on an input data sequence, and performing a left mirror permutation on the result of FFT and dividing with N, so as to obtain IFFT processing data;

the left mirror permutation is: inverting the sequence of the other data except the first data; the N is the length of the input data sequence.

5. The method according to claim 4, wherein performing the left mirror permutation on the result of FFT and dividing

with N is: after storing the result data sequence of FFT, when reading the result data sequence of FFT, firstly reading the first result data, then reading the other result data from back to front, namely, in inverted sequence, and dividing all the result data with N.

6. The method according to claim 4, wherein performing the left mirror permutation on the result of FFT and dividing with N is: after dividing the result data sequence of FFT with N and storing, when reading the result data sequence of FFT, firstly reading the first result data, then reading the other result data from back to front, namely, in inverted sequence.

7. The method according to claim 4, wherein performing the left mirror permutation on the result of FFT and dividing with N is: dividing the obtained result data sequence of FFT with N, and storing the other data except the first data in inverted sequence; and reading according to the storage sequence when reading the result data sequence of FFT.

8. The method according to claim 4, wherein performing the left mirror permutation on the result of FFT and dividing with N is: for the obtained result data sequence of FFT, storing the other data except the first data in inverted sequence; and reading according to the storage sequence when reading the result data sequence of FFT, and dividing with N.

9. An IFFT processing apparatus, comprising an FFT computing unit, further comprising a left mirror permutation unit; the left mirror permutation unit is used for receiving an input data sequence, inverting the sequence of the other data except the first data, and then outputting the data to the FFT computing unit;

the FFT computing unit is used for receiving the data output by the left mirror permutation unit, performing FFT, and dividing the result with N, so as to obtain IFFT processing data;

the N is the length of the input data sequence.

10. The apparatus according to claim 9, wherein the left mirror permutation unit is realized by a raw data storage unit.

11. An IFFT processing apparatus, comprising an FFT computing unit, further comprising a left mirror permutation unit;

the FFT computing unit is used for performing FFT on an input data sequence, and outputting a result data sequence of FFT to the left mirror permutation unit;

the left mirror permutation unit is used for receiving the result data sequence of FFT output by the FFT computing unit, inverting the sequence of the other data except the first data and dividing with N, so as to obtain IFFT processing data;

the N is the length of the input data sequence.

12. The apparatus according to claim 11, wherein the left mirror permutation unit comprises a computing result storage unit and a divider which are interconnected;

wherein, the computing result storage unit is used for performing the operation of inverting data sequence;

the divider is used for performing the operation of dividing the data related to inverting sequence with N.

13. The apparatus according to claim 12, wherein the connection relationship between the computing result storage unit and the divider is: an output side of the computing result storage unit is connected with an input side of the divider; or, an input side of the computing result storage unit is connected with an output side of the divider.

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